

**Amendments to the Specification:**

Please replace the abstract with the following amended abstract:

A universal memory device is presented that provides adaptability to existing hardware and software environments. The memory can "mimic" existing memory technology combining the advantages of integrating ~~in~~ all memory capacity ~~in to~~ into one single technology and still providing the implicit protections and access characteristics known from the different existing memory technologies. The memory device comprises a memory having ~~a plurality of~~ low-latency, rewritable, non-volatile memory cells ~~forming at least one memory section~~, a profile storage unit connected with ~~said the~~ memory and comprising access information allocated to ~~at least one~~ a set of request information elements (~~request~~ request profile), such that ~~said the~~ access information indicates whether ~~a request for access~~ an access request to said memory (~~access request~~), ~~said the~~ access request having ~~said the~~ request profile, is to be allowed or rejected, and an access control unit communicating with ~~said the~~ profile storage unit and ~~said the~~ memory, and adapted to allow or reject an incoming access request in dependence on the access information allocated to the request profile of the access request.